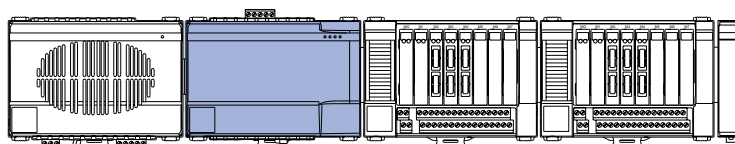


CONTROLLERS

OpenLine® Ethernet 586

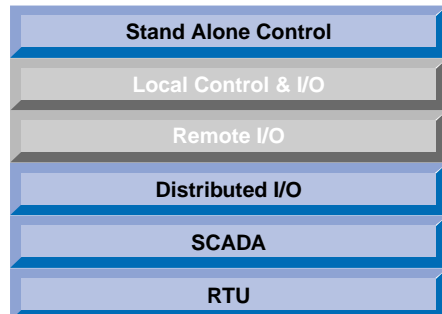
FEATURES

- 586-based CPU
- Each Unit Controls/Monitors up to 128 Local I/O Channels
- Any Mix of Analog or Digital I/O
- Executes On-Board Programs Written in Borland or Microsoft C++, Flow Chart, Ladder Logic, Function Block Diagrams, or Sequential Function Charts
- 10 MB per Second Communications Speed
- Modbus TCP Protocol Running Over TCP/IP
- CE Certified
- Operates in CSA Class I, Div 2 Environments
- DIN Rail or Panel Mount
- Redundant Operation
- Real Time Clock (Y2K Compliant)



ETHERNET CONTROLLER UNIT ON OPENLINE® SYSTEM

APPLICATIONS



GENERAL DESCRIPTION

Ethernet OpenLine® controllers plug directly into OpenLine® dual point module I/O bases. Any combination of 128 analog or digital I/O can be installed in the bases. The Ethernet ports on the controller support direct 10 Base-T connection using the RJ-45 connector or indirect AUI connections through a media access unit (MAU). The controllers IP address is stored on the FLASH disk. OpenLine® controllers share information with other OpenLine® controllers, third party devices and networked PCs using Modbus TCP/IP.

CPUs

The OpenLine® controllers are truly industrialized PCs. The controllers are powered by an AMD 586 processor. All controllers are presently shipped with a multi-threaded embedded DOS operating system. The rugged FLASH disk stores files exactly the same as a hard disk on a PC. DRAM is provided standard

at 4MB. This can be expanded to 64MB on some of the models.

PC104 FORM FACTOR

The internal construction of the Ethernet controller is based on the PC104 stack. This permits future CPU upgrades and expansion of the controller capabilities using add-in cards such as video cards, additional serial ports, quadrature decoder cards, etc.

EMBEDDED CONTROL PROGRAMS

ECPs can be uploaded and downloaded over the network. Once downloaded, ECPs execute as they would on a PC to service the communications ports and process I/O information. Executing local control programs result in much faster response times than are achievable over a network. In an OpenLine® system, the status of all 128 I/O channels are scanned and stored every 500 microseconds. The OpenLine® controllers can instantly access this data over the internal address/data bus using memory reads and writes.

In addition to faster response to I/O status changes, ECPs reduce the amount of network traffic, facilitate concurrent software development and simplify troubleshooting.

For C/C++ programmers, ECPs may be written and compiled with either Borland C++ or Microsoft C++ and downloaded to the controller. All compiler debugging tools can be used.

If you prefer to develop ECPs using Flow Charts, Ladder Logic, Function Block Diagrams, or Sequential Function Charts, we provide the ISaGRAF programming package. ISaGRAF is an IEC 1131 compliant software workbench which is easy to learn and use. Programs can be tested and debugged using ISaGRAF simulation prior to downloading the program into the OpenLine® controller. While the program is executing in the controller, the ISaGRAF monitoring application may be run to monitor the program execution and troubleshoot any remaining problems. Programming using ISaGRAF will save countless hours of development time. The programs are self-documenting, making it easier to troubleshoot or modify existing programs.

HOT BACKUP

OpenLine® systems are able to have two power supplies and controllers on the same I/O stack for applications requiring redundancy. One controller would be the primary and the other the secondary. Controllers and power supplies can be hot-swapped without disrupting the system's performance.

OTHER FEATURES

COM1 on the controller is an auxiliary RS-232 port that can be used for interrogating the CPU or talking to other RS-232 devices such as operator interface panels, printers, bar code readers, etc.

